

Electrochemical deposition of CdTe semiconductor thin films for solar cell application using two-electrode and three-electrode configurations: a comparative study

ECHENDU, O.K., OKEOMA, K.B., ORIAKU, C.I. and DHARMADASA, I.M.
<<http://orcid.org/0000-0001-7988-669X>>

Available from Sheffield Hallam University Research Archive (SHURA) at:

<http://shura.shu.ac.uk/12518/>

This document is the author deposited version. You are advised to consult the publisher's version if you wish to cite from it.

Published version

ECHENDU, O.K., OKEOMA, K.B., ORIAKU, C.I. and DHARMADASA, I.M. (2016). Electrochemical deposition of CdTe semiconductor thin films for solar cell application using two-electrode and three-electrode configurations: a comparative study. *Advances in Materials Science and Engineering*.

Copyright and re-use policy

See <http://shura.shu.ac.uk/information.html>

Electrochemical deposition of CdTe semiconductor thin films for solar cell application using two-electrode and three-electrode configurations: A comparative study

O. K. Echendu^{*1,2}, K. B. Okeoma², C. I. Oriaku³ and I. M. Dharmadasa¹

¹Electronic Materials and Sensors Group, Materials and Engineering Research Institute

Sheffield Hallam University, Sheffield S1 1WB, United Kingdom

²Thin Film Research Group, Department of Physics, Federal University of Technology,

P. M. B. 1526, Owerri, Nigeria

³Engineering Physics Group, Department of Physics, Michael Okpara University of Agriculture, Umudike, P. M. B. 7267, Umuahia, Nigeria

*Email: oechendu@yahoo.com Tel: +44(0)114 225 6910 Fax: +44(0)114 225 6930

Abstract

Thin films of CdTe semiconductor were electrochemically deposited using two-electrode and three-electrode configurations in potentiostatic mode for comparison. Cadmium sulphate and tellurium dioxide were used as cadmium and tellurium sources respectively. The layers obtained using both configurations exhibit similar structural, optical and electrical properties with no specific dependence on any particular electrode configuration used. These results indicate that electrochemical deposition (electrodeposition) of CdTe, and semiconductors in general, can equally be carried out using two-electrode system as well as the conventional three-electrode system without compromising the essential qualities of the materials produced. The results also highlight the advantages of the two-electrode configuration in process simplification, cost reduction and removal of a possible impurity source in the growth system especially as the reference electrode ages.

Keywords: Electrochemical deposition; two-electrode system; three-electrode system; CdTe; thin-films; solar cells.

1 Introduction

The electrochemical deposition (or simply electrodeposition) of CdTe for the fabrication of CdS/CdTe solar cells has received research attention for quite some time now [1-6]. The manufacturability and scalability of this simple but powerful process has been undoubtedly demonstrated by British Petroleum (BP Solar) company in the late 1990's by the production of solar panels of $\sim 1 \text{ m}^2$ with over 10% conversion efficiency [4]. The conventional

electrodeposition set-up involves the use of three electrode system (working electrode – the cathode, counter electrode – the anode and the reference electrode) and most of the work done so far on electrodeposition of semiconductors in general, have been on the basis of the three-electrode system [1-7]. It is well known that in the three-electrode system, the potential of the working electrode is measured relative to the reference electrode which itself has a well-known and stable electrode potential. As a result the potential on the working electrode is precisely controlled. This has the advantage of producing materials with controlled stoichiometry since the effects of changes in certain deposition parameters such as stirring, etc. are minimised. In the two-electrode system on the other hand, it is also known that the potential of the working electrode is measured relative to the counter electrode and the potential of the working electrode is not finely controlled as in the three-electrode system. The use of simple two-electrode configuration for electrodeposition of semiconductors is uncommon. As a result, only very few reports can be found in the literature involving the use of two-electrode system for the electrodeposition of compound semiconductors for solar cell fabrication [8-12].

In the electrodeposition of CdTe thin films in particular, reports on the use of two-electrode configuration are very scarce and the few available publications come principally from the authors' research group [8, 10-14]. This situation therefore prompted the use of two-electrode configuration alongside the conventional three-electrode configuration in the present work to study the possible effects of these different electrode configurations on the quality of CdTe layers produced as it regards their application in solar cell production. In addition, in the authors' research group, the suspicion that possible leakage of unwanted groups 1A and 1B ions like K^+ and Ag^+ from saturated calomel electrode (SCE) and Ag/AgCl reference electrodes could lead to the deterioration of the efficiency of CdTe solar cells as the reference electrode ages, gave impetus to the investigation of the use of the two-electrode system in the electrodeposition of CdTe and indeed other semiconductors. These ions are known to have severe detrimental effects on CdTe-based solar cells [6, 15]. The two-electrode approach therefore serves to eliminate one possible impurity source (the reference electrode) for the development of CdTe-based solar cells as well as to simplify the electrodeposition process and reduce cost at the same time. Again, the deposition temperature can be raised without the fear of exceeding the operating temperature limit of the reference electrode usually specified by the manufacturers ($\sim 70^\circ C$ for SCE and $\sim 100^\circ C$ for Ag/AgCl electrode). This will have the benefit of improving the crystallinity of the semiconductors deposited.

It is well-known that CdTe can be grown to have n-type conductivity, i-type conductivity or p-type conductivity simply by changing the stoichiometry of the material without involving external dopants. [13, 14, 16, 17]. A Cd-rich CdTe results in n-CdTe while a Te-rich CdTe results in p-CdTe [17]. In electrodeposition, this stoichiometry change is easily achieved by varying the deposition potential. At slightly lower cathodic deposition potentials from the potential of perfect stoichiometry (PPS), within the possible deposition potential range of CdTe, p-CdTe is obtained. At slightly higher cathodic deposition potentials from the PPS, n-CdTe is obtained [16], and at the PPS, intrinsic stoichiometric CdTe (i-CdTe) is obtained. This simply shows that such a material can be deposited within a certain range of applied potential. This fact has also been observed using two-electrode system and recently published

by the authors' group [18]. This same situation has also been reported for electrodeposited p-, i- and n-type copper indium diselenide (CIS) with applied deposition potential window of ~600 mV in two-electrode system [8] and p⁺-, p-, i-, n-, and n⁺-type copper indium gallium diselenide (CIGS), with applied deposition potential window of ~950 mV in three-electrode system [19]. In all these examples, it is therefore obvious that the electrodeposition of any particular semiconductor can actually take place over a certain range of applied potentials irrespective of the electrode system used. As another example, the work by Diso *et al.* [10] on two-electrode deposition of CdS layers shows that good quality CdS layers can be obtained within a deposition potential window of ~200 mV under the conditions they used. In the work by Takahashi *et al.* in ref [16] using three-electrode system, p-type and n-type CdTe were electrodeposited in applied cathodic deposition potentials from 300 mV to 600 mV vs. Ag/AgCl, which is actually a potential window of 300 mV.

Quite recently, the authors' group has reported CdTe-based solar cell efficiencies in the range (8 – 12)% [12, 20] while another group has reported high-efficiency CIGS solar cells [11], both using two-electrode systems. In any case, a comparative study of the use of both two-electrode and three-electrode configurations for the electrodeposition of semiconductor materials has never been reported. The present work therefore presents a comparative study of some properties of electrodeposited CdTe thin films grown using both two-electrode and three-electrode systems for thin-film solar cell applications.

2 Experimental Procedure

Two similar deposition electrolytes were used in the electrodeposition of CdTe layers in both electrode configurations. Both electrolytes contain aqueous solutions of 1M CdSO₄ of 99.0% purity and ~1 mM of 99.999% TeO₂ dissolved in H₂SO₄ all in 800 ml of deionized water. In addition, the electrolytes contain 1 mM each of 99.999% CdCl₂ and CdF₂ as sources of Cl and F atoms for n-type doping. All chemicals were obtained from Sigma Aldrich, United Kingdom. The use of the above mentioned dopants in this work follows from the fact that the halogens are well-known n-type dopants in CdTe, and they generally help to improve the efficiency of CdS/CdTe solar cells [6, 15, 21]. The two-electrode and three-electrode systems have high-purity carbon rod as the counter electrode (anode) and the three-electrode system has a saturated calomel electrode (SCE) as the reference electrode.

Before the addition of TeO₂, cyclic voltammograms of CdSO₄-only solutions were recorded for each electrode system using clean glass/fluorine-doped tin oxide (FTO) substrate as the working electrode (cathode) to determine the possible deposition potential of Cd. A computerised Gill AC potentiostat (ACM Instruments, Cumbria, UK) was used as the source of power. With this, electro-purification of the CdSO₄-only solutions was carried out at a temperature of 68.0±0.2°C for both electrode systems for 48 hours and at a cathodic potential slightly lower than the identified deposition potential of Cd as reported in a recent publication [12,14]. The aim of the electro-purification process is to eliminate from the solution, any possible detrimental metallic ions present in the CdSO₄ that may be incorporated into CdTe

during the main electrodeposition process due to the low purity of the CdSO_4 chemical. This process was carried out using a clean glass/FTO substrate as the working electrode where the possible impurities are deposited from the electrolyte. As an advantage of electrodeposition in general continuous removal of impurities takes place as electrodeposition progresses in a process known as self-purification. The temperature of $68.0 \pm 0.2^\circ\text{C}$ was used to avoid damaging the reference electrode since its maximum operating temperature is 70.0°C according to the manufacturer's specifications. TeO_2 solution was added after the electro-purification process and the pH of both electrolytes adjusted to 2.00 ± 0.02 . After stirring for 24 hours, another set of cyclic voltammograms was recorded to identify the possible range of deposition potentials for CdTe. Five samples were then deposited on clean glass/FTO of dimensions $3.0\text{ cm} \times 2.0\text{ cm} \times 3.0\text{ mm}$, from each electrolyte across the identified deposition potential range. This size of glass/FTO is also the same used for the voltammograms for uniformity. The deposited CdTe materials were then characterized using x-ray diffraction (XRD) measurements, photoelectrochemical (PEC) cell measurements and optical absorption measurements. From the characterisation results, the best deposition potential for CdTe for each electrode system was identified. The best layers were deposited on glass/FTO substrates at cathodic potentials of 1578 mV from the two-electrode system, and 697 mV vs. SCE from the three-electrode system. Prior to annealing at 450°C for 15 minutes in air for solar cell fabrication, the glass/FTO/CdTe samples were dipped in a saturated aqueous solution of CdCl_2 containing about 0.1 g of CdF_2 and then dried in air.

X-ray diffraction measurements were carried out using computerized Philips X'Pert Pro diffractometer (Philips Analytical, Almelo, Australia) with $\text{Cu-K}\alpha$ excitation wavelength of 1.5406 \AA in order to determine the crystal structure of the CdTe layers. PEC cell measurements were used to determine the electrical conductivity types of the CdTe layers deposited on glass/FTO. Optical absorption measurements were carried out using Cary 50 Scan UV-VIS spectrophotometer (Varian, Australia Pty Ltd) to determine the absorption behaviour and the energy bandgaps of the layers. The results of the structural, electrical and optical characterisations of the deposited CdTe layers are presented in the next section.

3 Results and Discussion

3.1 Cyclic voltammetry

Figures 1 (a) and (b) show the cyclic voltammograms of CdTe deposition electrolytes for (a) two-electrode system and (b) three-electrode system. When figure 1 (a) is zoomed in (see the inset), one can see that Te (with standard reduction potential $E^0 = +0.593\text{ V}$) begins to deposit at a cathodic potential of $\sim 200\text{ mV}$ in the forward sweep since it has a more positive standard reduction potential than Cd ($E^0 = -0.403\text{ V}$). Deposition of Cd is observed to begin from $\sim 800\text{ mV}$ as indicated by the noted increase of the current density. The deposition of Te-rich CdTe therefore starts from this point. Beyond this point, CdTe is obtained with higher deposition current density and increased Cd concentration. In the reverse sweep, the stripping off of Cd from the deposited layer is seen to begin at the point where the graph crosses to the negative

current density axis. Peaks in the negative current indicate the removal of elemental Cd and Cd from the CdTe layer. Finally, Te is removed at the lower cathodic voltages.

Good quality n-CdTe layers were deposited from this two-electrode system in the cathodic potential range of 1576 mV – 1580 mV (between points A and B) with the best layer appearing at a cathodic potential of ~1578 mV with a current density of ~4.50 mAcm⁻² at a moderate stirring rate. This deposition potential was chosen based on the most intense XRD peaks from the (111) crystallographic plane and the sharpest optical absorption edge.

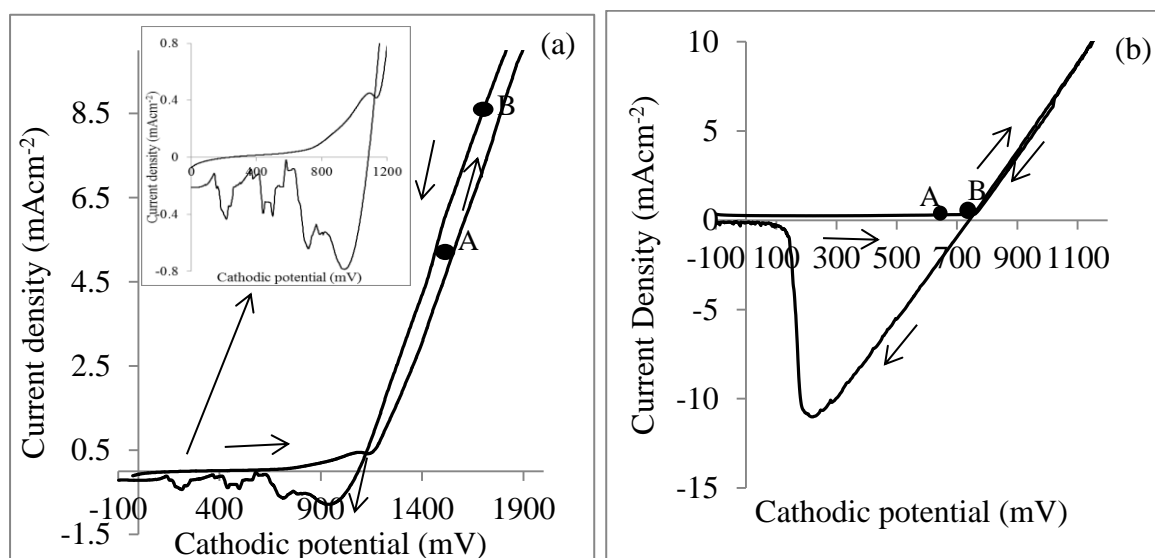


Figure 1: Cyclic voltammograms of CdTe deposition electrolytes for (a) two-electrode system and (b) three-electrode system.

In figure 1 (b), deposition of Te from the three-electrode system begins as soon as a cathodic potential is applied across the working electrode in the forward sweep. This may be due to the presence of CdCl₂ and CdF₂ in the electrolytes. The deposition of Cd also follows without any clear indication of the potential from where this happens. However, a sharp rise in the CdTe deposition current density sets in at a cathodic potential of ~750 mV. In the reverse scan, a large broad negative peak is observed. This broad peak represents the dissolution of elemental Cd, Cd from CdTe and the removal of Te from the cathode, in that order. From this system, good quality CdTe layers could be deposited in a cathodic potential ranging from 695 mV to 699 mV (between points A and B). However, just like in the case of the two-electrode system, the best cathodic deposition potential for this system was identified as 697 mV vs. SCE with a deposition current density of ~0.30 mAcm⁻² under moderate stirring. The observed lower deposition current density in the three-electrode system may be as a result of poorly cleaned glass/FTO used for the voltammogram, and does not in any way suggest poor performance of the three-electrode system. However, this current density is very much above the range of what we have always identified as optimum deposition current density (0.15 – 0.18) mAcm⁻² for CdTe for both electrode systems from experience. The deposition current densities in both cases were reduced to the range (0.15 – 0.18) mAcm⁻² during the CdTe deposition by reducing the stirring rate as is common in electrodeposition. In this current density range, the deposition is gradual and more uniform deposits are usually obtained. It is

worthy of note that the amount of Te ions in the electrolyte also affects the deposition current density. High Te ion content results in high current density and of course Te-rich CdTe since Te will always deposit first and easily crystallizes on the substrate.

3.2 X-ray diffraction (XRD) study

Figures 2 (a) and (b) show XRD patterns of the two sets of as-deposited CdTe samples grown using two-electrode and three-electrode system respectively. These samples in each case were grown for 1 hour at five different cathodic potentials within the identified deposition voltage range. Voltages were varied in steps of 1 mV in each case. The thicknesses of the samples are assumed to be comparable without much disparity since the surface areas of the glass/FTO substrates are the same and the average deposition current densities for the two systems do not differ by so much (in the range of 0.15 – 0.18 mAcm⁻²) and the deposition time is the same. In electrodeposition, the thickness, T , of the deposited film for any given electrolyte, is theoretically estimated using the Faraday's equation ($T = MJt/nF\rho$) in which the deposition current density, J , (which takes into account the surface area of the film) and the deposition time, t , are the major variables. M is the molecular weight of the material deposited, n is the number of electrons transferred in the process, ρ is the density of the material deposited and F is the Faraday's constant. Both figures 2(a) and (b) show the presence of the strong preferential orientation of the prominent peak corresponding to the (111) crystallographic plane of cubic CdTe material. The other visible peaks present belong to the underlying FTO substrate. In fact, based on the peak intensities of the (111) preferential orientation, the best cathodic growth voltages can be seen clearly from both figures. For the two-electrode system, this corresponds to 1578 mV while for the three-electrode system it corresponds to 697 mV. From these figures also one notes that the as-deposited CdTe grown using these different electrode configurations are similar in structural quality showing the same preferential orientation of the crystallites in the (111) crystal plane.

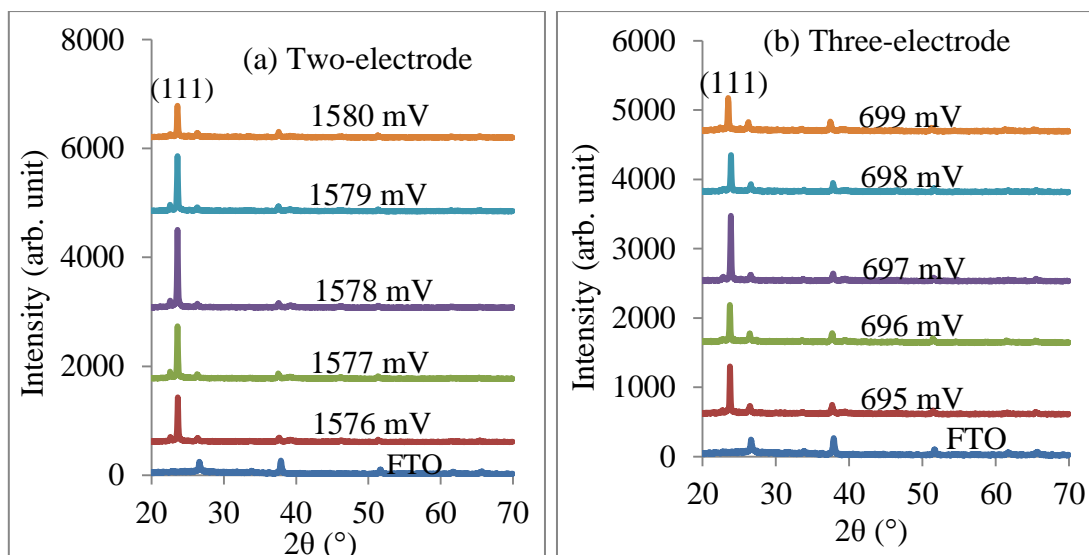


Figure 2: XRD patterns of as-deposited CdTe layers grown at five different cathodic voltages for 1 hour using (a) two-electrode system and (b) three-electrode system.

Figures 3 (a) and (b) show the XRD patterns of the annealed samples of figure 2. Again the (111) preferential orientation is very prominent for both electrode systems at all deposition voltages.

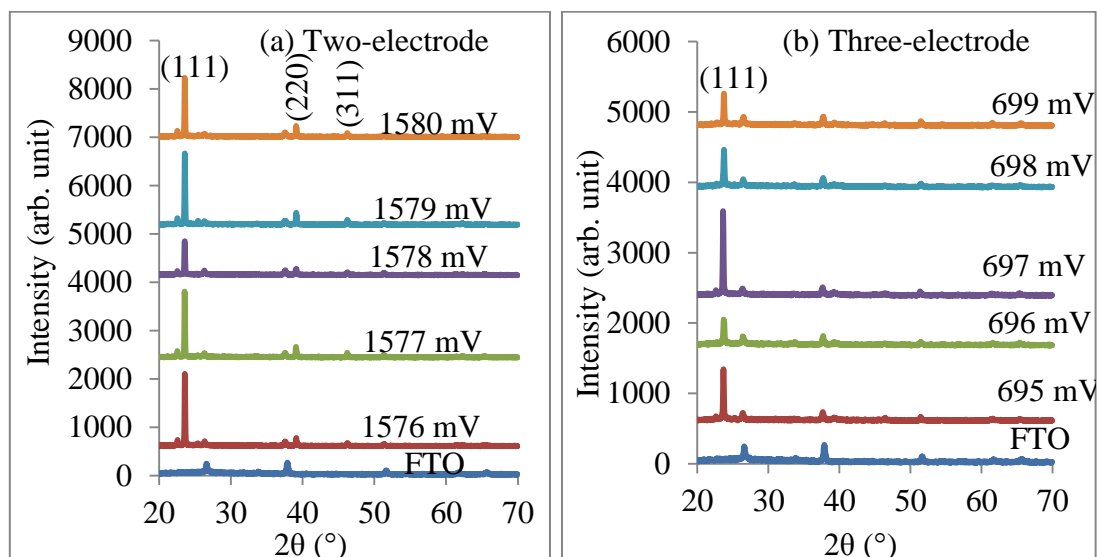


Figure 3: XRD patterns of CdTe layers grown at different cathodic voltages for 1 hour using (a) two-electrode system and (b) three-electrode system, and annealed at 450°C for 15 minutes.

There is also a gradual emergence of two other peaks corresponding to reflections from (220) and (311) crystal planes of the same cubic CdTe. These two peaks are more visible in figure 3 (a) than in figure 3 (b). This slight difference may be due to difference in the effective deposition rate as a result of slight difference in deposition current densities in the two different deposition electrolytes. In fact it was generally observed in the course of this work that the two-electrode system always showed higher deposition current density compared to the three-electrode system. The overall average deposition current density maintained for the deposition of all the samples in the two-electrode system at moderate stirring rate of 400 rotations per minute was $\sim 0.18 \text{ mAcm}^{-2}$ while that for the three-electrode system was $\sim 0.15 \text{ mAcm}^{-2}$. High deposition current densities usually result in very quick depositions with tellurium-richness as seen from the very shiny layers obtained. Again, the post-deposition annealing in $\text{CdCl}_2 + \text{CdF}_2$ environment could result in the appearance of these additional (220) and (311) XRD peaks since they are not observed in the as-deposited samples. It is possible that there may have been present in the deposited CdTe, some unreacted Te as well as Cd which eventually reacted in the annealing process in the presence of both heat (at 450°C) and $\text{CdCl}_2 + \text{CdF}_2$ mixture to give rise to better crystalline and stoichiometric CdTe materials, in a process of re-crystallisation, which resulted in the appearance of the additional XRD peaks. This situation could have been more pronounced in the two-electrode system.

In the two-electrode system (figure 3 (a)), there appears to be a shift in the best deposition voltage in terms of the intensity of the (111) peak. The best voltage in this case appears to be 1576 mV. This shift from the original values of 1578 mV must have to do with re-crystallisation which also resulted in the appearance of the (220) and (311) peaks as already mentioned. In the case of the three-electrode system, the best growth voltage remains 697 mV based on the highest (111) XRD peak intensity. The samples in figure 3 were annealed at 450°C for 15 minutes after $\text{CdCl}_2 + \text{CdF}_2$ treatment. The XRD data of CdTe materials from both electrode configurations match the Joint Committee on Powder Diffraction and Standards (JCPDS) reference file No. 00-015-0770. The crystallite sizes estimated using the Scherrer equation for the (111) peaks of CdTe materials from both electrode systems are comparable with values in the ranges of 18.7 – 49.8 nm for as-deposited materials and 18.7 – 77.0 nm for annealed materials.

3.3 Photoelectrochemical (PEC) cell study

Table 1 shows the PEC cell signal results of as-deposited CdTe layers grown using two-electrode system and three-electrode system. The results show that the as-deposited CdTe layers were n-type in electrical conduction irrespective of the electrode configuration used and the PEC signal values of samples from both electrode configurations are comparable.

Table 1: PEC signal results of as-deposited CdTe layers grown using the two-electrode system and three-electrode system.

V _g (mV)	Two-electrode (As-deposited)				V _g (mV)	Three-electrode (As-deposited)			
	V _D (mV)	V _L (mV)	PEC (V _L -V _D) (mV)	Type		V _D (mV)	V _L (mV)	PEC (V _L -V _D) (mV)	Type
1576	-192	-264	-72	n	695	-208	-269	-62	n
1577	-168	-222	-54	n	696	-219	-292	-73	n
1578	-195	-251	-56	n	697	-195	-256	-61	n
1579	-159	-249	-90	n	698	-200	-278	-78	n
1580	-190	-261	-71	n	699	-255	-305	-50	n

PEC cell measurement is a quick way of determining the conductivity types of semiconductors especially when they are deposited on conducting substrates like glass/FTO as used in this work. In this situation, standard methods such as Hall Effect measurement could not be used, due to the presence of the underlying conducting substrate upon which the CdTe layers are grown. This is because, in the Hall Effect measurement, current would naturally prefer to flow through the conductive path of least resistance (i.e. through the conducting substrate) instead of through the CdTe semiconductor under investigation thereby producing misleading results. For this reason, PEC measurement becomes the only alternative for the determination of the electrical conductivity types of these semiconductors grown on conducting substrates as it is extremely difficult in most cases to detach the layers from the FTO substrates.

The PEC cell technique is based on the formation of a solid (semiconductor)/liquid (electrolyte) junction when a semiconductor is brought into intimate contact with a suitable electrolyte. A Schottky-type potential barrier is formed at the semiconductor/electrolyte interface and the direction of band-bending in the semiconductor depends on the electrical conductivity type of the semiconductor. The voltage across the FTO and a counter electrode (carbon in our case) is recorded under dark condition using a voltmeter. The system is then illuminated using white light and voltage across the two terminals is recorded again. The difference between the voltage under illumination (V_L) and that under dark condition (V_D)

gives the PEC signal. The sign of the PEC signal is then used to determine the conductivity type of the semiconductor involved. n-type and p-type semiconductors have opposite PEC signals for the same electrolyte. If an n-type semiconductor has a negative PEC signal for any given electrolyte, then a p-type semiconductor will have a positive PEC signal for the same electrolyte. This is because these two semiconductors have band bending in opposite directions when in contact with the electrolyte. The magnitude of the PEC signal gives an indication of the level of doping concentration in the semiconductor. Large PEC signals indicate moderate doping and hence formation of healthy depletion region. A heavily doped semiconductor will have a low PEC signal due to the formation of a thin depletion region. If a metal or an insulator is used in place of the semiconductor, zero PEC signal will be registered in both cases as there is no band-bending associated with these types of materials. The PEC measurements reported in this work were carried out using aqueous solution of 0.1 M $\text{Na}_2\text{S}_2\text{O}_3$ as an electrolyte and a digital voltmeter.

3.4 Spectrophotometry

Figures 4 (a) and (b) show the optical absorption spectra of as-deposited CdTe layers using the two-electrode system and three-electrode system respectively. The absorption edges of the as-deposited samples for both electrode configurations are not sharp. This is as a result of poor crystalline quality and probably poor stoichiometry of the as-deposited CdTe materials especially since they were grown at relatively low temperature as mentioned earlier.

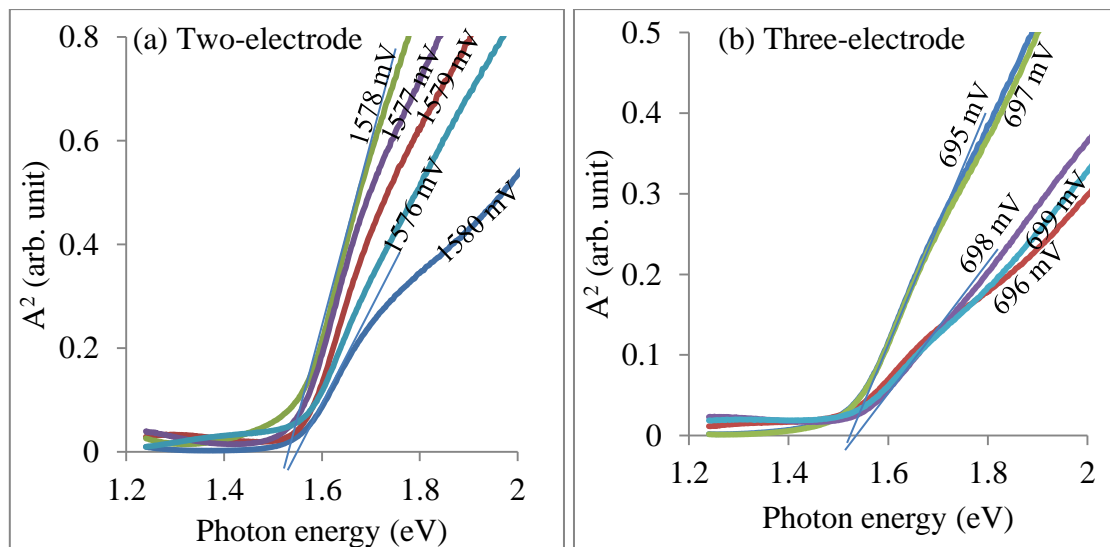


Figure 4: Square of absorbance (A^2) vs. photon energy for as-deposited CdTe samples grown with (a) two-electrode system and (b) three-electrode system for 1 hour at different cathodic voltages.

However, the absorption edges of the three-electrode-grown samples are slightly weaker than those of two-electrode system perhaps due to the reasons given earlier for the XRD result. The energy bandgaps obtained for all the materials are however similar for both electrode configurations, giving values in the range (1.53 - 1.55) eV, which is above the bulk value of 1.45 eV for CdTe for the same reason of poor crystallinity and stoichiometry given above.

Figures 5 (a) and (b) also show the optical absorption of the same CdTe samples of figure 4 after annealing at 450 °C for 15 minutes. Clearly, there is significant improvement in the absorption edges of the samples after annealing. This is certainly due to the improvement in both crystallinity and stoichiometry of these materials as a result of the post-deposition heat treatment in the presence of $\text{CdCl}_2 + \text{CdF}_2$.

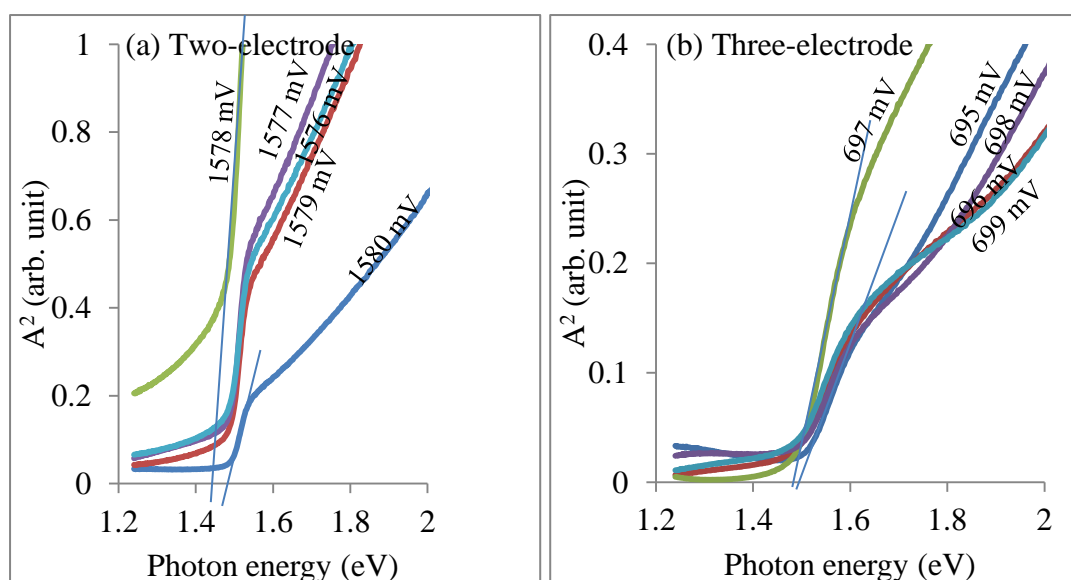


Figure 5: Square of absorbance (A^2) vs. photon energy of annealed CdTe samples grown with (a) two-electrode system and (b) three-electrode system at different cathodic voltages for 1 hour.

The improvement in crystalline quality after annealing is also evident from the XRD results presented earlier. The overall improvement in quality by the annealing process is also seen in the improved absorption edges of all the CdTe materials. The energy bandgaps of the annealed CdTe materials from both electrode configurations are narrower than those of as-deposited samples and fall in the range (1.45-1.48) eV. These bandgap values are also closer

to the bulk value, showing that the quality of the materials has improved considerably after annealing.

4 Conclusion

A comparative study of the electrodeposition and characterisation of CdTe thin films on glass/FTO substrates using both two-electrode configuration and three-electrode configuration has been presented. X-ray diffraction, photoelectrochemical cell measurements and optical absorption spectrophotometry results show that the electrodeposited CdTe layers from both electrode configurations possess similar and comparable structural, optical and electronic properties. The results further highlight the possible advantages of the use of the two-electrode configuration in the electrodeposition of semiconductor materials for possible device fabrication without compromising the essential device qualities of these materials. Elimination of a possible impurity source (the reference electrode), process simplification, ability to grow better materials at high temperatures and cost reduction, are some of these advantages achievable through the use of the two-electrode configuration over three-electrode configuration.

Acknowledgement

Pilkington group Ltd, UK, is thanked for providing glass/FTO substrates for this work. The principal author is thankful to the Tertiary Education Trust Fund, Nigeria and the Federal University of Technology, Owerri, Nigeria for financial support.

Conflict of interest

The authors declare that there is no conflict of interest regarding the publication of this paper.

References

- [1] M. P. R. Panicker, M. Knaster and F. A. Kröger, *J. Electrochem. Soc.* **125** (1978) 566.
- [2] Basol B. M. *J. Appl. Phys.* **55** (1984) 601.
- [3] Basol B. M. *Solar Cells* **23** (1988) 69.
- [4] Cunningham D., Rubcich M. and Skinner D. *Prog. Photovolt: Res. App.* **10** (2002) 159.
- [5] Duffy N. W., Peter L. M. and Wang R. L., *J. Electroanal. Chem.* **532** (2002) 207.

- [6] Dennison S., *J. Mater. Chem.* **4** (1) (1994) 41.
- [7] Kroger F. A., *J. Electrochem. Soc.* **124** (1978) 2038.
- [8] Dharmadasa I. M., Burton R. P. and Simmond M., *Sol. Energy Mat. Sol. Cells* **90**(2006) 2191.
- [9] Bouraiou A., Aida M. S., Tomasella E. and Attaf N., *J. Mater. Sci.* **44** (2009) 1241.
- [10] Diso D. G., Muftah G. A., Patel V. and Dharmadasa I. M., *J. Electrochem. Soc.* **157** (6) (2010) H647.
- [11] Bhattacharya R. N., *Sol. Energ. Mater. Sol. Cell.* **113** (2013) 96.
- [12] Echendu O. K., Fauzi F., Weerasinghe A. R. and Dharmadasa I. M., *Thin Solid Films* **556** (2014) 529.
- [13] Diso D. G., Research and development of CdTe-based thin film solar cells (Doctoral thesis), *Sheffield Hallam University, United Kingdom* (2011). <http://shura.shu.ac.uk/id/eprint/4941>.
- [14] Dharmadasa I. M., Bingham P. A., Echendu O. K., Salim H. I., Druffel T., Dharmadasa R. *et al. Coatings* **4** (2014) 380.
- [15] Zanio K., *Semiconductors and Semimetals, vol. 13 "Cadmium Telluride"*, New York: Academic Press (1978).
- [16] Takahashi M., Uosaki K. and Kita H., *J. Appl. Phys.* **60** (6) (1986) 2046.
- [17] Patil V. B., Sutrave D. S., Shahane G. S. and Deshmukh L. P., *Thin Solid Films* **401** (2001) 35.
- [18] D. G. Diso, F. Fauzi, O. K. Echendu, O. I. Olusola and I. M. Dharmadasa, *J. Mater Sc: Mater Electron* (online first, May) (2016). Doi: 10.1007/s10854-016-4844-3.
- [19] Dharmadasa I. M., Chaure N. B., Tolan G. J. and Samantilleke A. P., *J. Electrochem. Soc.* **154** (6) (2007) H466.
- [20] O. K. Echendu and I. M. Dharmadasa, *Energies* **8** (2015) 4416. Doi: 10.3390/en8054416.
- [21] Mazzamuto S., Vaillant L., Bosio A., Romeo N., Armani N. and Salviati G., *Thin Solid Films* **516** (2008) 7079.